

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. IS9-017		/SERIAL NO. 09/904, 578	
LIST OF ARTICLES BY APPLICANT (See several sheets, if necessary)				APPLICANT Richard C. Eden et al.			
				FILING DATE July 12, 2001		GROUP 2812	
U.S. PATENT DOCUMENTS							
*Examiner/ Initial/		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
M	AA	5,910,669	6/8/99	Chang et al.			
	AB	5,532,179	7/2/96	Chang et al.			
	AC	5,900,764	5/4/99	Imam et al.			
	AD	5,973,367	10/26/99	Williams			
	AE	6,114,726	9/5/00	Barkhordarian			
	AF	5,548,150	8/20/96	Omura et al.			
	AG	5,736,766	4/7/98	Efland et al.			
	AH	5,479,089	12/26/95	Lee			
	AI	5,479,117	12/26/95	Gabara			
	AJ	5,418,064	9/15/92	Kevorkian et al.			
	AK	5,728,594	3/17/98	Efland et al.			
	AL	5,859,456	1/12/99	Efland et al.			
	AM	6,150,722	11/21/00	Efland et al.			
M	AN	6,097,064	8/1/00	Saitoh et al.			
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes No
	AO						
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
M	AR		"Design of Pulse Width Modulation Based Smart Switch Controller for Automotive Application; Athalye et al.; 10/99; pps. 8.C.1-1 - 1-7.				
	AS		"Analysis and Implementation of a Semi-integrated Buck Converter with Static Feedback Control"; Strandberg et al.; 6/29/99; 4 pps.				
	AT		"Role of Semiconductor Devices in Portable Electronics Power Management"; Mohamed Darwish; 8/9/00; 4 pps.				
	AU		"Motorola to Preview Next-generation Smart Power Technology at World's Leading Power Conference"; <a href="http://www.apspg.com/press/060100/smart.html">http://www.apspg.com/press/060100/smart.html</a> ; 6/29/01; 2 pps.				
M	AV		"TEA1204T: High Efficiency DC/DC Converter"; <a href="http://www.semiconductors.philips.com/pip/tea1204t.html">http://www.semiconductors.philips.com/pip/tea1204t.html</a> ; 6/29/01; 3 pps.				
EXAMINER M/m ✓ M/V				DATE CONSIDERED 8-23-02			
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<div style="display: flex; align-items: center; justify-content: center;"> <div style="border: 2px solid black; border-radius: 50%; padding: 10px; text-align: center; margin-right: 10px;"> O I P E APR 22 2002 PATENT &amp; TRADEMARK OFFICE </div> <div> LIST OF PART CITED BY APPLICANT (Use several sheets if necessary) </div> </div>					APPLICANT Richard C. Eden et al.			
					FILING DATE July 12, 2001		GROUP 2812	
U.S. PATENT DOCUMENTS								
*Examiner/ Initial/	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate		
EE	AA	6,329,801 B1	12/11/01	Zuniga et al.				
EE	AB	6,278,264 B1	8/21/01	Burnstein et al.				
EE	AC	6,271,651 B1	8/7/01	Stratakos et al.				
EE	AD	6,268,716 B1	7/31/01	Burnstein et al.				
EE	AE	6,225,795 B1	5/1/01	Stratakos et al.				
EE	AF	6,198,261 B1	3/6/01	Schultz et al.				
EE	AG	6,160,441	12/12/00	Stratakos et al.				
EE	AH	6,100,676	8/8/00	Burnstein et al.				
EE	AI	6,031,361	2/29/00	Burnstein et al.				
EE	AJ	6,020,729	2/1/00	Stratakos et al.				
EE	AK	5,969,513	10/19/99	Clark				
EE	AL	5,744,843	4/28/98	Efland et al.				
EE	AM	6,150,150	11/21/00	Livi et al.				
EE	AN	6,351,108 B1	2/26/02	Burnstein et al.				
FOREIGN PATENT DOCUMENTS								
	Document Number	Date	Country	Class	Subclass	Translation		
						Yes	No	
	AO							
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)								
EE	AR		"Synchronous Rectification with Adaptive Timing Control"; Acker et al.; 06/95; pps. 88-95.					
	AS		"High-Efficiency Low-Voltage DC-DC Conversion for Portable Applications"; Stratakos; The 12th International Symposium on Power Semiconductor Devices & ICs; 1998; pps. 1-245.					
	AT		"A Low Voltage CMOS Compatible Power MOSFET for On-Chip Dynamic Voltage Scaling DC-DC Converters" Nassif-Khalil et al.; 1/2000; pps. 43-46					
EE	AU		"FlipFET™ MOSFET Design for High Volume SMT Assembly"; Schofield et al.; <a href="http://www.irf.com/technical-info/papers.html">http://www.irf.com/technical-info/papers.html</a> ; 3/18/2001; 6 pps.					
EXAMINER N/A				DATE CONSIDERED 8-23-02				
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<div style="display: flex; align-items: center;"> <div style="border: 2px solid black; border-radius: 50%; padding: 10px; margin-right: 10px; text-align: center;"> O I P E APR 27 2002 J2C22 PATENT &amp; TRADEMARK OFFICE </div> <div> LIST OF ART CITED BY APPLICANT  (Use several sheets if necessary) </div> </div>				APPLICANT Richard C. Eden et al.			
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U.S. PATENT DOCUMENTS							
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	AA						
	AB						
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FOREIGN PATENT DOCUMENTS							
	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
H	AO		"Active Clamp Circuits for Switchmode Regulators Supplying Microprocessor loads" Sanders et al.; 5/ 1997; pps. 1179-1185.				
H	AP		"An Active Clamp Circuit for Voltage Regulation Module (VRM) Applications"; Wu et al.; IEEE Transactions on Power Electronics, Vol. 16, No. 5; 9/2001; pps. 623-634				
H	AQ		"An Integrated Controller for a High Frequency Buck Converter"; Lau et al.; 5/1997; pps. 246-254.				
H	AR		"A Low-Voltage CMOS DC-DC Converter for a Portable Battery-Operated System"; Stratakis et al.; 5/1994; pps. 619-626.				
H	AS		"International Rectifier Introduces HexFET® Power MOS in FlipFET™ Packages Enabling World's Highest Power Density"; <a href="http://www.irf.com/whats-new/nr000612a.html">http://www.irf.com/whats-new/nr000612a.html</a> ; June 2000; 2 pps.				
H	AT		U.S. Patent Application Pub. No. US 2001/0038277 A1; Publication Date 11/8/2001; Burnstein et al.				
H	AU		U.S. Patent Application Pub. No. US 2001/0035746 A1; Publication Date 11/1/2001; Burnstein et al.				
EXAMINER <i>N/p m</i> ✓ <i>N/</i>				DATE CONSIDERED <i>8-25-02</i>			
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2812

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*Examiner/ Initial/		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
<i>M</i>	AA	4,256,977	3/17/1981	Hendrickson	—	—	
	AB						
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## FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
<i>M</i>	AO	JP406260927A	9/16/1994	Komatsu (Japan)	—	—	Abstract	

## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

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*Mjm* ✓ *N*

DATE CONSIDERED

8-23-02

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